

Network Solutions Oki. for a Global Society

FEDR27V1602F-01-02 Issue Date: Jul. 9, 2004

OKI Semiconductor MR27V1602F

1,048,576–Word × 16–Bit or 2,097,152–Word × 8–Bit One Time PROM

GENERAL DESCRIPTION

The MR27V1602F is a 16 Mbit electrically One Time Programmable Read-Only Memory that can be electrically switched between 1,048,576-word \times 16-bit and 2,097,152-word \times 8-bit by the state of the BYTE# pin. The MR27V1602F supports high speed asynchronous read operation using a single 3.3V power supply.

FEATURES

 \cdot 1,048,576-word \times 16-bit/2,097,152-word \times 8-bit electrically switchable configuration

- \cdot +3.3 V power supply
- · Access time 80 nS MAX
- Operating current 16 mA MAX (5MHz)
- Standby current $10 \,\mu A \,MAX$
- · Input/Output TTL compatible
- · Tri-state output
- · Packages:

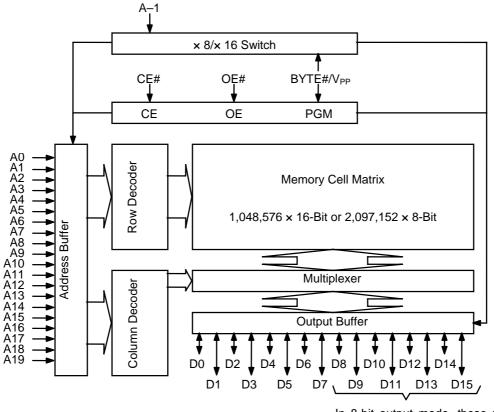
44-pin plastic SOP (SOP44-P-600-1.27-K) (MR27V1602FMA) 48-pin plastic TSOP (TSOP(1)48-P-1220-0.50-1K) (MR27V1602FTN)

				/ /	1
			A15 1	0	48 A16
			A14 2		47 BYTE#/V _{PP}
NC 1	0	44 NC	A13 3		46 V _{SS}
A18 2		43 A19	A12 4		45 D15/A–1
A17 3		42 A8	A11 5		44 D7
A7 4		41 A9	A10 6		43 D14
A6 5		40 A10	A9 7		42 D6
A5 6		39 A11	A8 8		41 D13
A4 7		38 A12	A19 9		40 D5
A3 8		37 A13	NC 10		39 D12
A2 9		36 A14	NC 11		38 D4
A1 10		.35 A15	NC 12		37 V _{CC}
A0 11		34 A16	NC 13		36 D11
CE# 12		33 BYTE#/V _{PP}	NC 14		35 D3
V _{SS} 13		32 V _{SS}	NC 15		34 D10
OE# 14		.31 D15/A–1	A18 16		33 D2
D0 15		.30 D7	A17 17		32 D9
D8 16		29 D14	A7 18		31 D1
D1 17		28 D6	A6 19		30 D8
D9 18		27 D13	A5 20		29 D0
D2 19		.26 D5	A4 21		28 OE#
D10 20		25 D12	A3 22		27 V _{SS}
D3 21		24 D4	A2 23		26 CE#
D11 22		23 V _{CC}	A1 24		25 A0
I	44-pin SOP		Л	8-pin TSOP	(1)
			4	0-pin 130P	(1)

PIN CONFIGURATION (TOP VIEW)

Pin name	Functions				
D15/A–1	Data output/Address input				
A0 to A19	Address input				
D0 to D14	Data output				
CE#	Chip enable				
OE#	Output enable				
BYTE#/V _{PP}	Mode switch/Program power supply voltage				
Vcc	Power supply voltage				
V _{SS}	GND				
NC	Non connection				

BLOCK DIAGRAM



In 8-bit output mode, these pins are placed in a high-Z state and pin D15 functions as the A-1 address pin.

FUNCTION TABLE

Mode	CE#	OE#	BYTE#/V _{PP}	V _{CC}	D0 to D7	D8 to D14	D15/A–1	
Read (16-Bit)	L	L	Н			D _{OUT}		
Read (8-Bit)	L	L	L		D _{OUT}	Hi–Z	L/H	
Output disable		Н	Н	221/		Hi–Z		
Output disable		п	L	3.3 V		*		
Standby	н		Н			Hi–Z		
Standby		*	L			HI-Z		
Program	L	Н			D _{IN}			
Program inhibit	Н	Н	8.2 V	4.0 V		Hi–Z		
Program verify	Н	L				D _{OUT}		

*: Don't Care (H or L)

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Condition	Value	Unit
Operating temperature under bias	Та		0 to 70	°C
Storage temperature	Tstg	_	-55 to 125	°C
Input voltage	VI		–0.5 to V _{CC} +0.5	V
Output voltage	Vo	rolotivo to V	–0.5 to V _{CC} +0.5	V
Power supply voltage	Vcc	relative to V _{SS}	–0.5 to 5	V
Program power supply voltage	V _{PP}		-0.5 to 9.0	V
Power dissipation per package	PD	Ta = 25°C	1.0	W
Output short circuit current	los	_	10	mA

RECOMMENDED OPERATING CONDITIONS

(Ta = 0 to 70°C)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
V _{CC} power supply voltage	Vcc		3.0		3.6	V
V _{PP} power supply voltage	V _{PP}		-0.5	-	V _{CC} +0.5	V
Input "H" level	V _{IH}	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$	2.2	_	V _{CC} +0.5*	V
Input "L" level	VIL		-0.5**	_	0.6	V

Voltage is relative to V_SS. * : Vcc+1.5V(Max.) when pulse width of overshoot is less than 10ns.

** : -1.5V(Min.) when pulse width of undershoot is less than 10ns.

ELECTRICAL CHARACTERISTICS

DC Characteristics

			(`	V _{CC} = 3.3 V :	± 0.3 V, Ta =	0 to 70°C)
parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Input leakage current	ILI	$V_1 = 0$ to V_{CC}	—	—	10	μA
Output leakage current	I _{LO}	$V_{O} = 0$ to V_{CC}	—	—	10	μA
V _{CC} power supply current	I _{CCSC}	$CE\# = V_{CC}$	—	—	10	μA
(Standby)	ICCST	$CE\# = V_{IH}$	—	—	1	mA
V _{CC} power supply current (Read)	I _{CCA}	$CE\# = V_{IL}, OE\# = V_{IH}$ f=5MHz	—	—	16	mA
V _{PP} power supply current	I _{PP}	$V_{PP} = V_{CC}$	—	—	10	μA
Input "H" level	VIH	—	2.2	—	V _{CC} +0.5*	V
Input "L" level	VIL	—	-0.5**	—	0.6	V
Output "H" level	V _{OH}	I _{OH} = -2 mA	2.4	_	_	V
Output "L" level	V _{OL}	$I_{OL} = 4 \text{ mA}$	_	_	0.4	V

Voltage is relative to V_{SS}.

* : Vcc+1.5V(Max.) when pulse width of overshoot is less than 10ns.

** : -1.5V(Min.) when pulse width of undershoot is less than 10ns.

AC Characteristics

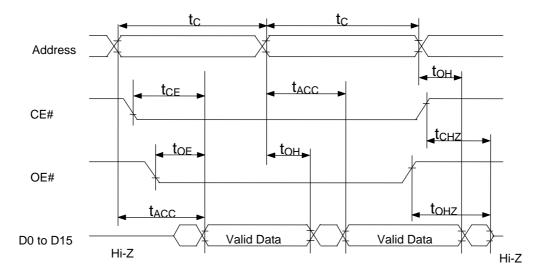
			$(V_{CC} =$	3.3 V ± 0.3 V, Ta	a = 0 to 70°C)
Parameter	Symbol	Condition	Min.	Max.	Unit
Address cycle time	tc	—	80	—	ns
Address access time	t _{ACC}	$CE\# = OE\# = V_{IL}$	_	80	ns
CE# access time	t _{CE}	$OE\# = V_{IL}$	_	80	ns
OE# access time	t _{OE}	$CE\# = V_{IL}$	—	30	ns
Output disable time	t _{CHZ}	$OE\# = V_{IL}$	0	20	ns
Output disable time	t _{OHZ}	$CE\# = V_{IL}$	0	20	ns
Output hold time	t _{OH}	$CE\# = OE\# = V_{IL}$	0	—	ns

Measurement conditions

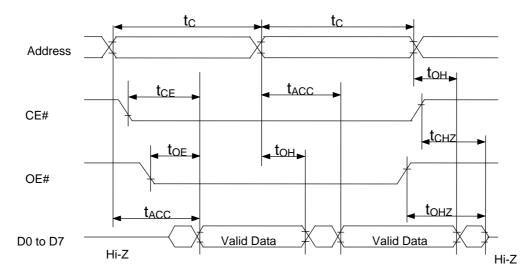
Output load

TIMING CHART (READ CYCLE)

16-BIT READ MODE (BYTE# = V_{IH})



8-BIT READ MODE (BYTE# = V_{IL})



ELECTRICAL CHARACTERISTICS (PROGRAMMING OPERATION)

DC Characteristics

					(Ta = 2	5°C ± 5°C)
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Input leakage current	Ιu	$V_I = V_{CC}$ +0.5 V	_	_	10	μA
V _{PP} power supply current (Program)	I _{PP2}	$CE\# = V_{IL}$	—	_	50	mA
V _{CC} power supply current	Icc	—	_	_	50	mA
Input "H" level	V _{IH}	—	3.0	_	V _{CC} +0.5	V
Input "L" level	VIL	—	-0.5	_	0.8	V
Output "H" level	V _{OH}	I _{OH} = -400 μA	2.4	_	_	V
Output "L" level	V _{OL}	I _{OL} = 2.1 mA	_	_	0.45	V
Program voltage	V _{PP}		7.95	8.2	8.45	V
V _{CC} power supply voltage	V _{CC}	—	3.9	4.0	4.1	V

Voltage is relative to V_{SS}.

AC Characteristics

	$(V_{CC} = 4.0 \text{ V} \pm 0.1 \text{ V}, \text{BYTE} # / V_{PP} = 8.2 \text{ V} \pm 0.25 \text{ V}, \text{Ta} = 25^{\circ}\text{C} \pm 5^{\circ}\text{C})$								
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit			
Address set-up time	t _{AS}	_	100	_	—	ns			
OE# set-up time	t _{OES}	_	2	_	—	μS			
Data set-up time	t _{DS}	—	100	—	—	ns			
Address hold time	t _{AH}	_	2	_	—	μS			
Data hold time	t _{DH}	—	100	_	—	ns			
Output float delay time from OE#	t _{OHZ}	—	0	—	100	ns			
V _{PP} voltage set-up time	t _{VS}	_	2	_	—	μS			
Program pulse width	t _{PW}	_	9	10	11	μS			
Data valid from OE#	t _{OE}	_	—	_	100	ns			
Address hold from OE# high	t _{AOH}	_	0	_	_	ns			

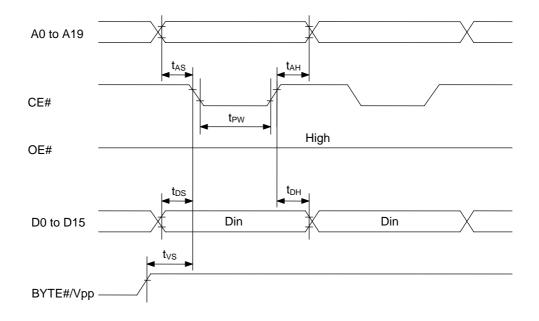
Pin Check Function

Pin Check Function is to check contact between each device-pin and each socket-lead with EPROM programmer. Setting up address as following condition call the preprogrammed codes on device outputs.

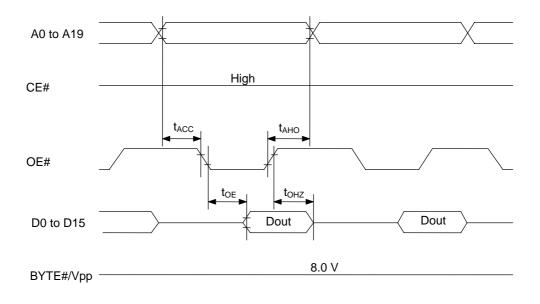
$(V_{CC} = 3.3 \text{ V} \pm 0.1 \text{ V}, \text{ CE#} = V_{IL}, \text{ OE#} = V_{IL}, \text{ BYTE#}/V_{PP} = V_{IH}, \text{ Ta} = 25^{\circ}\text{C} \pm 10^{\circ}\text{C}$										C ± 5°C)										
A0	A1	A2	A3	A4	A5	A6	A7	A8	A9	A10	A11	A12	A13	A14	A15	A16	A17	A18	A19	DATA
0	1	0	1	0	1	0	1	0	VH*	0	1	0	1	0	1	0	0	1	1	FF00
1	0	1	0	1	0	1	0	1	VH*	1	0	1	0	1	0	1	1	0	0	00FF
	Other conditions										FFFF									

*: VH = 7.0V ± 0.25 V

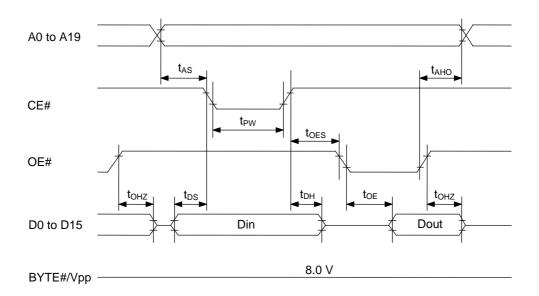
Consecutive Programming Waveforms



Consecutive Program Verify Waveforms



Program and Program Verify Cycle Waveforms

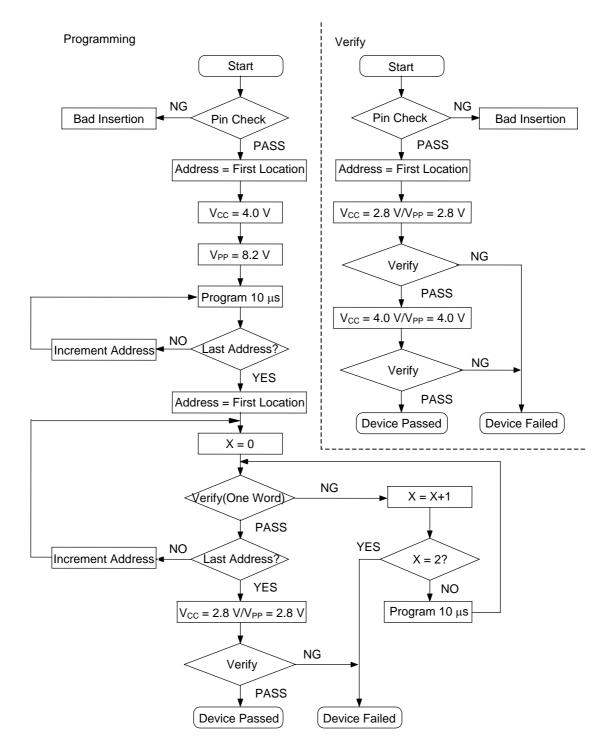


Pin Capacitance

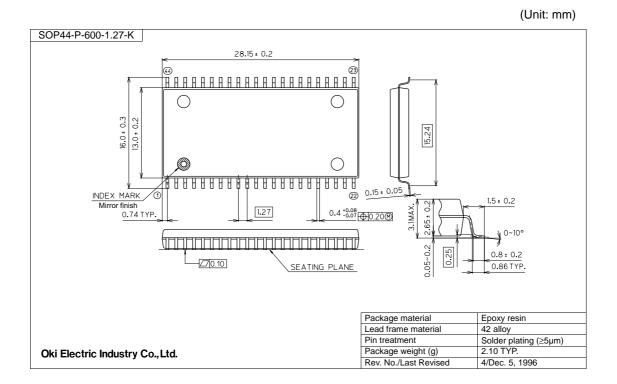
(V_{CC} = 3.3 V, Ta = 25°C, f = 1 MHz)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Input	C _{IN1}	$V_1 = 0 V$	—	—	8	
BYTE#/V _{PP}	C _{IN2}	$v_1 = 0 v$	_	_	120	pF
Output	C _{OUT}	$V_0 = 0 V$	_	_	10	

Programming/Verify Flow Chart



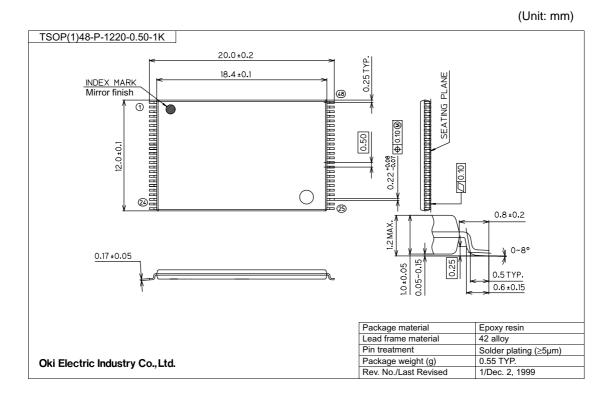
PACKAGE DIMENSIONS



Notes for Mounting the Surface Mount Type Package

The surface mount type packages are very susceptible to heat in reflow mounting and humidity absorbed in storage.

Therefore, before you perform reflow mounting, contact Oki's responsible sales person for the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).



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REVISION HISTORY

Document		Pa	ge			
No.	Date	Previous Edition	Current Edition	Description		
FEDR27V1602F-01-01	Apr. 20, 2004	-	-	Final edition 1		
FEDR27V1602F-01-02	Jul. 9, 2004	4	4	Add P_D condition and $I_{OS} = 10mA$		

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